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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/821,606	04/08/2004	Sang H. Dhong	END920030125US1 (17131)	1561
23389 7590 02/14/2008 SCULLY SCOTT MURPHY & PRESSER, PC 400 GARDEN CITY PLAZA SUITE 300 GARDEN CITY, NY 11530				
EXAMINER DO, CHAT C				
ART UNIT 2193		PAPER NUMBER		
MAIL DATE 02/14/2008		DELIVERY MODE PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/821,606

Applicant(s)

DHONG ET AL.

Examiner

CHAT C. DO

Art Unit

2193

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 03 December 2007.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 08 April 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/5508)
- Paper No(s)/Mail Date _____

- 4) ☐ Interview Summary (PTO-413)
- Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

1. This communication is responsive to Amendment filed 12/03/2007.
2. Claims 1-20 are pending in this application. Claims 1 and 13 are independent claims.

This Office Action is made final.

Drawings

3. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the limitations cited in claims 3 and 9 must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will

be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 101

4. 35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

5. Claims 1-20 are rejected under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter.

Claims 1-20 cite a unit and method for performing multiply/add operations in accordance with a predetermined mathematical operations. However, claims merely disclose series mental steps/components for performing multiply/add operations without disclosing a practical application. Further, the claims appear to preempt every substantial practical applications of the idea embodied by the claims. Therefore, claims 1-20 are directed to non-statutory subject matter.

Claim Rejections - 35 USC § 102

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

7. Claims 1-5 and 11-17 are rejected under 35 U.S.C. 102(b) as being anticipated by Elliott et al. (U.S. 5,880,983).

Re claim 1, Elliott et al. disclose in Figures 2 a floating point execution unit (e.g. Figure 2) for performing multiply/add operations (e.g. abstract and col. 3 lines 39-46) using a plurality of operands taken from an instruction having a plurality of operand positions (e.g. corresponding to A, B, and C operands as mentioned in col. 1 lines 21-50), the floating point unit (e.g. Figure 2) comprising: a multiplier (e.g. component 102 in Figure 2B) for calculating a product of two of the operands (e.g. as output of multiplier 102 with A and C as input operands in Figure 2B); an aligner (e.g. components first and second aligners 118 in Figures 2A and 2B) coupled to the multiplier for aligning said product and a third of the operands (e.g. third operand is B as seen in Figure 2A); a first data path for supplying to the multiplier operands from a first and a second of the operand positions of the instruction (e.g. first data path for feeding A and C operands into multiplier 102 in Figure 2B); a second data path for supplying the third operand to the aligner (e.g. data path for feeding B into muxes 114 and 116 in Figure 2A); and a multiplexer (e.g. muxes 114 and 116 in Figure 2A) on the second data path for selecting, for use by the aligner, either the operand from the second operand position of the instruction (e.g. operand A) or the operand from the third operand position of the instruction (e.g. operand B as seen in Figure 2A).

Re claim 2, Elliott et al. further disclose in Figures 2 the first data path is maintained free of multiplexer operations (e.g. Figure 2B wherein the operands A and C operands are fed directly into the multiplier 102).

Re claim 3, Elliott et al. further disclose in Figures 2 aligner includes means to compute a shift amount for aligning said product and the third operand (e.g. components

111 and 118 in Figure 2A); and the multiplexer operates to select the third operand in parallel with the means to compute the shift amount (e.g. components 114 or 116 in Figure 2A).

Re claim 4, Elliott et al. further disclose in Figures 2 multiplexer selects the third operand while the means to compute computes said shift amount (e.g. Figure 2A).

Re claim 5, Elliott et al. further disclose in Figures 2 each of the operands and said product includes an exponent value (e.g. as part of floating point number system as seen in col. 1 lines 21-57), and the means to compute computes said shift amount based only on said exponent values (e.g. inherently as for adding correctly as seen in Figure 2A).

Re claim 11, Elliott et al. further disclose in Figures 2 the means to compute the shift amount compresses two of the three input exponents and an offset while selecting the third exponent (e.g. two of three inputs exponents are belong to addends as seen in Figure 2A).

Re claim 12, Elliott et al. further disclose in Figures 2 when executing an add or subtract instruction, the means to compute the shift amount computes the alignment shift amount as $ea+eb-2eb$ (e.g. Figure 2A as difference between the adding operand in order to align properly).

Re claim 13, it is a method claim having similar limitations cited in claim 1. Thus, claim 13 is also rejected under the same rationale as cited in the rejection of rejected claim 1.

Re claim 14, it is a method claim having similar limitations cited in claim 2.
Thus, claim 14 is also rejected under the same rationale as cited in the rejection of rejected claim 2.

Re claim 15, it is a method claim having similar limitations cited in claim 3.
Thus, claim 15 is also rejected under the same rationale as cited in the rejection of rejected claim 3.

Re claim 16, it is a method claim having similar limitations cited in claim 4.
Thus, claim 16 is also rejected under the same rationale as cited in the rejection of rejected claim 4.

Re claim 17, it is a method claim having similar limitations cited in claim 5.
Thus, claim 17 is also rejected under the same rationale as cited in the rejection of rejected claim 5.

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 6-8 and 18-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Elliott et al. (U.S. 5,880,983) in view of Willson Jr. et al. (U.S. 7,228,325).

Re claims 6-8, Elliott et al. further disclose in Figures 2 each of the operands has an exponent value, and further comprising means, operating in parallel with the multiplier

and the aligner, to determine whether the exponent values of any of the operands is zero while the multiplier calculates said product to establishes a test result number. However, Willson Jr. et al. disclose each of the operands has an exponent value, and further comprising means, operating in parallel with the multiplier and the aligner, to determine whether the exponent values of any of the operands is zero (e.g. col. 7 line 60 to col. 8 line 15) while the multiplier calculates said product to establishes a test result number (e.g. as for bypass the adder and claims 30-31). Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention is made to add each of the operands has an exponent value, and further comprising means, operating in parallel with the multiplier and the aligner, to determine whether the exponent values of any of the operands is zero while the multiplier calculates said product to establishes a test result number as generally seen in concept in willson Jr. et al.'s invention into Elliott et al.'s invention because it would lower power consumption (e.g. col. 7 lines 50-65 due to bypassing the adder/multiplication computation).

Re claim 18, it is a method claim having similar limitations cited in claim 6. Thus, claim 18 is also rejected under the same rationale as cited in the rejection of rejected claim 6.

Re claim 19, it is a method claim having similar limitations cited in claim 7. Thus, claim 19 is also rejected under the same rationale as cited in the rejection of rejected claim 7.

Response to Arguments

6. Applicant's arguments filed 12/03/2007 have been fully considered but they are not persuasive.

- a. The applicant comments in page 7 last paragraph for drawing objection that the claimed subject matter of claims 3 are shown in Figures 2-3 and 4-8.

The examiner respectfully submits that there is no Figure 8 as commented by the applicant. In addition, Figures 2-3 does not show the structure of claim 3 as the aligner includes means to compute a shift amount for aligning said product and the multiplexer operates to select the third operand in parallel with the means to compute the shift amount.

- b. The applicant argues in page 9 second paragraph for claims rejected under 35 U.S.C. 101 that the claims, particularly claims 1 and 13, do not stand outside the scope of section 101 since it increases the performance speed of a floating point execution unit as of....in the second paragraph of page 9.

The examiner respectfully submits that the feature and its support of "increases the performance speed of a floating point execution unit" as alleged by the applicant above is not seen in the claim. In general, the claims just merely disclose series of mental steps for performing multiply/add operations. Thus, claims 1-20 are directed to non-statutory subject matter.

c. The applicant argues in pages 11-12 last paragraphs for claims rejected under 35 U.S.C. 102(b) that the cited data path that feeds B into muxes 114 and 116 is not equivalent to applicants' second data path wherein the applicants' Figure 3 shows a data path to the aligner that includes no muxes. Further, the cited reference by Elliot et al. fail to disclose a multiplexer on the second data path for selecting either operand from the second operand or from the third operand wherein Elliot muxes only A&B and the multiply output AC.

The examiner respectfully submits that the cited reference by Elliot et al. clearly and expressively disclose in Figures 2 all the limitations cited in claims 1 and 13, particularly the limitations argument above wherein Figure 2 discloses operands A, B, and C as second, third, and first operands. The claims require an aligner (e.g. 118 in Figure 2A) that would couple to the multiplier (e.g. 102 in Figure 2B) and third operands (e.g. B operand) and a multiplexer (e.g. 114 as multiplexer in Figure 2A) for selecting either second operand (e.g. A operand) or the third operand (e.g. B operand). Therefore, the cited reference by Elliot et al. clearly meet all the limitations cited in claimed invention, particularly claims 1 and 13.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO

MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chat C. Do whose telephone number is (571) 272-3721. The examiner can normally be reached on M => F from 7:00 AM to 5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lewis Bullock can be reached on (571) 272-3759. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Chat C. Do
Examiner
Art Unit 2193

February 15, 2008

/Chat C. Do/

Art Unit: 2193

Primary Examiner, Art Unit 2193

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10/821,606

Examiner

CHAT C. DO

**Applicant(s)/Patent under
Reexamination**

DHONG ET AL.

Art Unit

2193